

S/N 09/945512

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes Examiner: Richard Booth
Serial No.: 09/945512 Group Art Unit: 2812
Filed: August 30, 2001 Docket: 1303.027US1
Title: IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL
BARRIER INTERPOLY INSULATORS

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LEONARD FORBES

By his Representatives,

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Date 6-9-03

By M. L. B.

Maryvin L. Beekman

Reg. No. 38,377

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, P.O.Box 1450, Alexandria, VA 22313-1450, on this 97 day of June, 2003

1400, on this 1 day of June, 2005
Andrew J. Kael
Name

John J. Morris
Signature

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%; padding: 2px;">Application Number</td> <td style="width: 50%; padding: 2px;">09/945512</td> </tr> <tr> <td>Filing Date</td> <td>August 30, 2001</td> </tr> <tr> <td>First Named Inventor</td> <td>Forbes, Leonard</td> </tr> <tr> <td>Group Art Unit</td> <td>2812</td> </tr> <tr> <td>Examiner Name</td> <td>Booth, Richard</td> </tr> </table>					Application Number	09/945512	Filing Date	August 30, 2001	First Named Inventor	Forbes, Leonard	Group Art Unit	2812	Examiner Name	Booth, Richard
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		Attorney Docket No: 1303.027US1														
Sheet 1 of 2																

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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	US-2001/0055838	12/27/2001	Walker, A J., et al.	438	129	08/13/2001
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Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		AARIK, JAAN , et al., "Anomalous effect of temperature on atomic layer deposition of titanium oxide", <u>Journal of Crystal Growth</u> , (2000), pp. 531-537	
		AARIK, JAAN , et al., "Texture development in nanocrystalline hafnium dioxide thin films grown by atomic layer deposition", <u>Journal of Crystal Growth</u> , (2000), pp. 105-113	
		FERGUSON, J D., et al., "Atomic layer deposition of Al ₂ O ₃ and SiO ₂ on BN particles using sequential surface reaction", <u>Applied Surface Science</u> , (2000), pp. 280-292	
		KIM, YONG S., et al., "Effect of rapid thermal annealing on the structure and the electrical properties of atomic-layer-deposited Ta ₂ O ₅ films", <u>Journal of the Korean Physical Society</u> , (December 2000), pp. 975-979	
		KIM, YEONG K., et al., "Novel capacitor technology for high density stand-alone and embedded DRAMs", <u>IEEE</u> , (2000), 4 pages	
		KUKLI, KAUPO , "Atomic Layer Deposition of Titanium Oxide from TiI ₄ and H ₂ O ₂ ", <u>Chemical Vapor Deposition</u> , (2000), pp. 303-310	
		KUKLI, KAUPO , et al., "Atomic layer deposition of zirconium oxide from zirconium tetraiodide, water and hydrogen peroxide", <u>Journal of Crystal Growth</u> , (2001), pp. 262-272	
		KUKLI, KAUPO , et al., "Real-time monitoring in atomic layer deposition of TiO ₂ from TiI ₄ and H ₂ O-H ₂ O ₂ ", <u>American Chemical Society</u> , (2000), pp. 8122-8128	

EXAMINER

DATE CONSIDERED

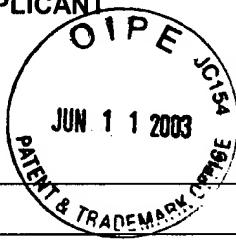
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



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Attorney Docket No: 1303.027US1	

Sheet 2 of 2

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		LEE, J. , "Effect of Polysilicon Gate on the Flatband Voltage Shift and Mobility Degradation for ALD-Al ₂ O ₃ Gate Dielectric", <u>IEDM</u> , (2000), pp. 645-648	
		PARANJPE, AJIT , et al., "Atomic layer deposition of AlO _x for thin film head gap application", <u>Journal of the Electrochemical Society</u> , (September 2001), pp. 465-471	
		SMITH, RYAN C., et al., "Chemical vapour deposition of the oxides of titanium, zirconium and hafnium for use as high-k materials in microelectronic devices. A carbon-free precursor for the synthesis of hafnium dioxide", <u>Advanced Materials for Optics and Electronics</u> , (2000), 2 pages	

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